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U.S. UTILITY Patent Application

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APF	PLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
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Brenor Brophy Xiao Xi Dinesh Nadavi

RSL 2/84

Architecture, circuitry and method for controlling a subsystem through a JTAG access port

PTO-2040 12/99

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TERMINAL		DRAWINGS	İ	CLAIMS ALLOWED		
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subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)			
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not extend beyond the expiration date of U.S Patent. No				ISSUE FEE		
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orm PTO-436A lev. 6/99)			FILED WITH:	DISK (CRF)	FICHE CD-RC	